



10/796110

COFC

Docket No.: M4065.1020/P1020
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Larry D. Kinsman

Patent No.: 6,979,902

Issued: December 27, 2005

For: CHIP SIZE IMAGE SENSOR CAMERA
MODULE

Certificate
FEB 13 2006
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted an error which should be corrected.

In Claim 45, the PTO made the following error to be corrected:

Column 8, lines 49-50, "surface near the top of the substrate." should read -- surface.--.

The error was not in the application as filed or amended by Applicant; accordingly no fee is required.

FEB 14 2006

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: February 7, 2006

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

FEB 14 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 6,979,902
APPLICATION NO. : 10/796,110
ISSUE DATE : December 27, 2005
INVENTOR : Larry D. Kinsman

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 45, the following error is corrected:

Column 8, lines 49-50, "surface near the top of the substrate." should read --surface.--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Thomas J. D'Amico
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 1
2101 L Street NW
Washington, DC 20037-1526

FEB 14 2006